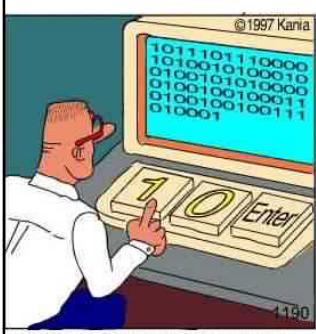


Software Visualization



Lecture WS 02/03

Visualizing the Results
of Program Analyses

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Static Program Analyses

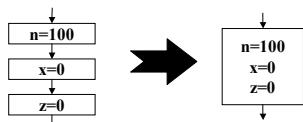
- Control Flow Analysis
- Data Flow Analysis
- Visualizing Analysis Results

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Control Flow Graphs

- Remark:
 - Often sequences of statements are combined into a single node called *basic block*.



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Static Program Analysis

- Computes properties of a program, which hold for all executions of a program.
 - Not every property can be computed because of the Halting Problem
- Dynamic vs. Static properties
 - How often is a program point executed for a given input (dynamic)
 - Is a program point not executed for any input. (static)

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Control Flow Analysis

- Computes the control flow graph of a program
 - Computation was very easy for the sample language we used before!
 - Problems for real programming languages:
 - Function/procedure calls → interprocedural CFG
 - Function pointers (in higher-order functional languages, but also in C)
 - Dynamic dispatch in object oriented languages like Java

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Data Flow Analysis

- Computes information for each program point (node in the CFG) about the data that will reach this program point during execution.
- In general two kinds of information are computed for each node v :
 - $\text{IN}(v)$: information about the state before the program point is executed.
 - $\text{OUT}(v)$: information about the state after the program point is executed.
- Method: Locally available information is propagated over the paths in the control flow graph.

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Data Flow Analysis

- Forward Flow Problem: what can happen before control reaches this program point, e.g.:
 - Reaching definitions
 - Available expressions
- Backward Flow Problem: what can happen after control leaves this program point, e.g.:
 - Live variables
 - Very busy expressions
 - Reached uses

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Available Expressions

- A binary expression $e_1 \text{ op } e_2$ is **available** at program point p if it has been computed before, i.e. along each path by which p can be reached.
- program optimization: create temporary variable to avoid recomputation.
- Forward Flow Problem

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Data Flow Analysis: Available Expressions

Given a control flow graph $(V, E, \text{in}, \text{out})$. Let \mathcal{E} be the set of all binary expressions which occur in the program.

$$\begin{aligned} \text{GEN}, \text{KILL} : V &\rightarrow L_G(E) \\ \text{GEN}(v) = &\begin{cases} \{e'|e' \text{ is a binary subexpression of } e\} & \text{if } v = e \\ \{e'|e' \text{ is a binary subexpression of } e \text{ and } x \text{ does not occur in it}\} & \text{if } v = x=e \\ \emptyset & \text{otherwise} \end{cases} \\ \text{KILL}(v) = &\begin{cases} \{e'|e' \in \mathcal{E} \text{ and } x \text{ occurs in } e'\} & \text{if } v = x=e \\ \emptyset & \text{otherwise} \end{cases} \end{aligned}$$

Algorithm:

```

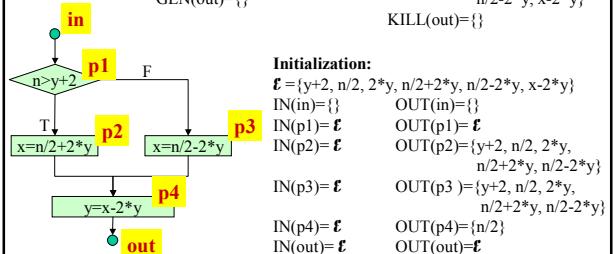
 $IN(\text{in}) = \emptyset$ 
 $OUT(\text{in}) = \emptyset$ 
for all  $v \in (V - \{\text{in}\})$  do
   $IN(v) = \mathcal{E}$ 
   $OUT(v) = (IN(v) - KILL(v)) \cup GEN(v)$ 
while there are changes do
  for all  $v \in (V - \{\text{in}\})$  do
     $IN(v) = \bigcap_{(p,t,v) \in E} OUT(p)$ 
     $OUT(v) = (IN(v) - KILL(v)) \cup GEN(v)$ 
  
```

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Example: Available Expressions

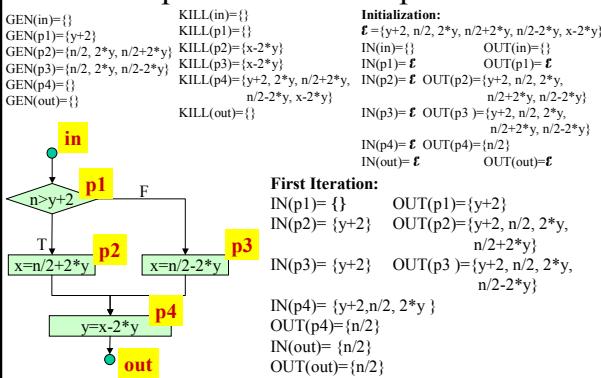
$$\begin{aligned} \text{GEN}(\text{in}) &= \emptyset & \text{KILL}(\text{in}) &= \emptyset \\ \text{GEN}(p1) &= \{y+2\} & \text{KILL}(p1) &= \emptyset \\ \text{GEN}(p2) &= \{n/2, 2*y, n/2+2*y, x-2*y\} & \text{KILL}(p2) &= \{x-2*y\} \\ \text{GEN}(p3) &= \{n/2, 2*y, n/2-2*y\} & \text{KILL}(p3) &= \{x-2*y\} \\ \text{GEN}(p4) &= \emptyset & \text{KILL}(p4) &= \{y+2, 2*y, n/2+2*y, \\ & & & n/2-2*y, x-2*y\} \\ \text{GEN}(\text{out}) &= \emptyset & \text{KILL}(\text{out}) &= \emptyset \end{aligned}$$



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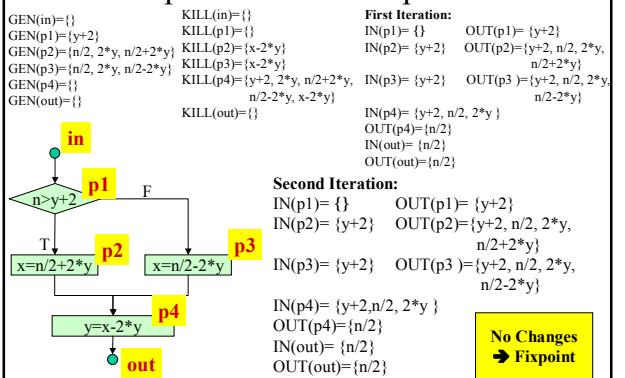
Example: Available Expressions



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Example: Available Expressions



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Live Variables

- A variable x is **live** at program point p if there is a path from p to p' and
 - x is used at p' (occurs in an expression)
 - There is no redefinition (assignment to x) of x along that path.
- Backward Flow Problem

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Data Flow Analysis: Live Variables

$GEN, KILL : V \rightarrow L_G(V)$

$$KILL(v) = \begin{cases} \{x\} & \text{if } v = x = e \\ \emptyset & \text{otherwise} \end{cases}$$

$$GEN(v) = \begin{cases} \{x' | x' \text{ occurs in } e\} & \text{if } v \in \{x=e, e\} \\ \emptyset & \text{otherwise} \end{cases}$$

Algorithm:

for all $v \in V$ do

$$IN(v) = GEN(v)$$

while there are changes do

for all $v \in V$ do

$$OUT(v) = \bigcup_{(v,l,s) \in E} IN(s)$$

$$IN(v) = (OUT(v) - KILL(v)) \cup GEN(v)$$

In particular
 x' can be x .

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Example: Live Variables

GEN(in)={} KILL(in)={}	INITIALIZATION: IN=GEN
GEN(p1)={n}	KILL(p1)={}
GEN(p2)={n,y}	KILL(p2)={}
GEN(p3)={n}	KILL(p3)={n}
GEN(p4)={n}	KILL(p4)={y}
GEN(out)={}	KILL(out)={}

First Iteration:
OUT(in)={n} IN(in)={n}
OUT(p1)={n,y} IN(p1)={n,y}
OUT(p2)={n} IN(p2)={n,y}
OUT(p3)={n,y} IN(p3)={y,n}
OUT(p4)={n,y} IN(p4)={n}
OUT(out)={} IN(out)={}

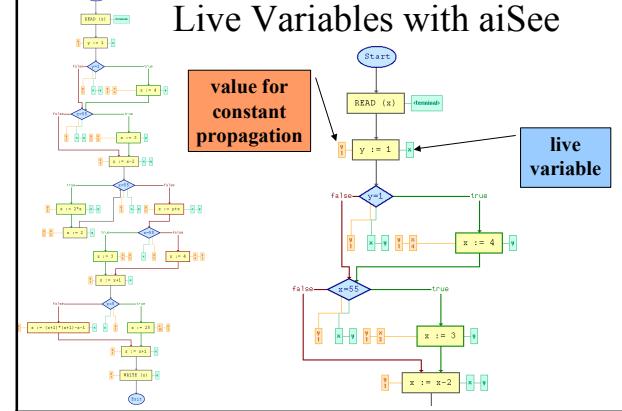
Second Iteration:
OUT(in)={n} IN(in)={n}
OUT(p1)={n,y} IN(p1)={n,y}
OUT(p2)={y,n} IN(p2)={n,y}
OUT(p3)={n,y} IN(p3)={y,n}
OUT(p4)={n,y} IN(p4)={n}
OUT(out)={} IN(out)={}

Third Iteration: No Changes \rightarrow Fixpoint
--

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Live Variables with aiSee



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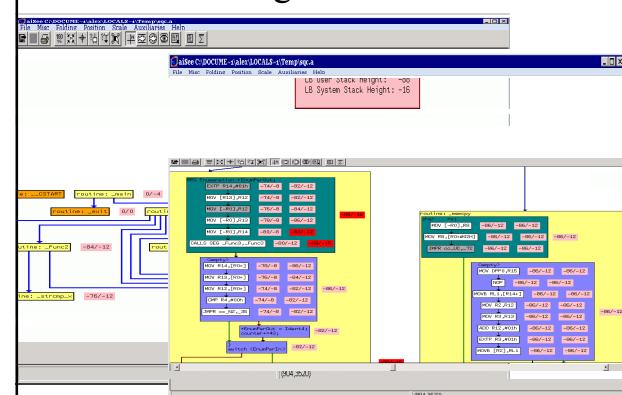
Other Static Analyses

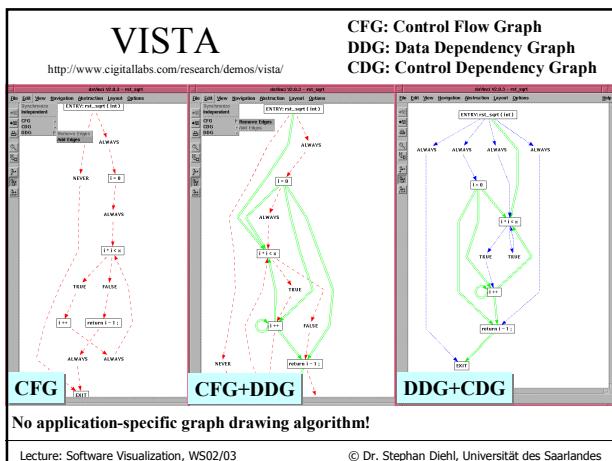
- Stack usage
- Types
- Worst Case Execution Times

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Stack Usage Visualization





- Control Dependence Graph:
 - statements are only dependent on their preceding condition or the entry node
 - similar to Jackson Diagrams

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